

REMARKS

The Examiner has rejected claims 1, 2, 9 and 10 under the judicially created doctrine of obviousness-type double patenting as being unpatentable over claims 1, 7 and 9 of co-pending U.S. Patent Application Serial No. 10/536,640 in view of Japanese Patent Application No. JP02078031A to Onishi et al. The Examiner has further rejected claims 3 and 4 under the judicially created doctrine of obviousness-type double patenting as being unpatentable over claims 1, 7 and 9 of co-pending U.S. Patent Application Serial No. 10/536,640 in view of Onishi et al., and further in view of U.S. Patent 6,661,415 to Yasuda et al. In addition, the Examiner has rejected claim 5 under the judicially created doctrine of obviousness-type double patenting as being unpatentable over claims 1, 7 and 9 of co-pending U.S. Patent Application Serial No. 10/536,640 in view of Onishi et al., and further in view of U.S. Patent 4,609,813 to Cohen. Furthermore, the Examiner has rejected claim 6 and 7 under the judicially created doctrine of obviousness-type double patenting as being unpatentable over claims 1, 7 and 9 of co-pending U.S. Patent Application Serial No. 10/536,640 in view of Onishi et al., and further in view of Japanese Patent Application No. JP61-248253 to Morimoto.

In response thereto, enclosed herewith is a Terminal Disclaimer referencing co-pending U.S. Patent Application Serial No. 10/536,640.

In view of the above, Applicants believe that the Examiner's rejections have been overcome.

Applicants believe that this application, containing claims 1-7, 9 and 10, is now in condition for allowance and such action is respectfully requested.

Respectfully submitted,

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